

FIG. 1 (Prior Art)

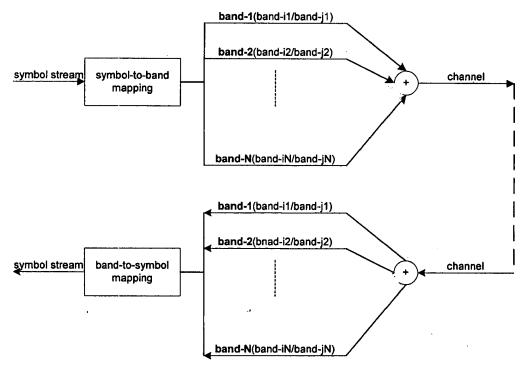
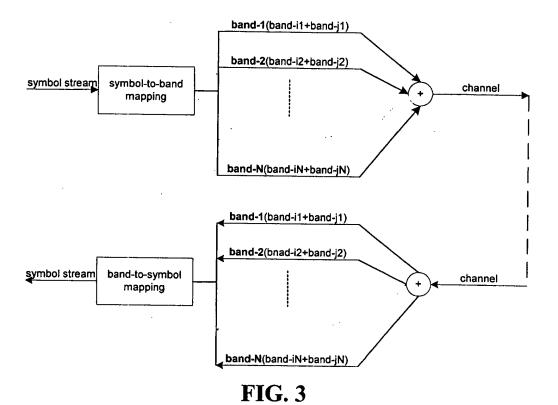


FIG. 2



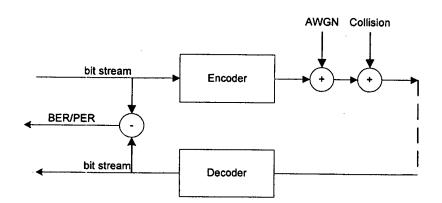


FIG. 4

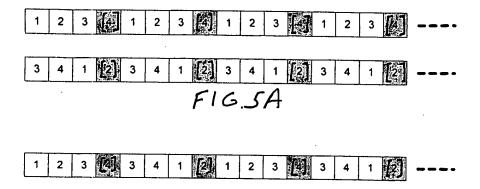


FIG. 5B

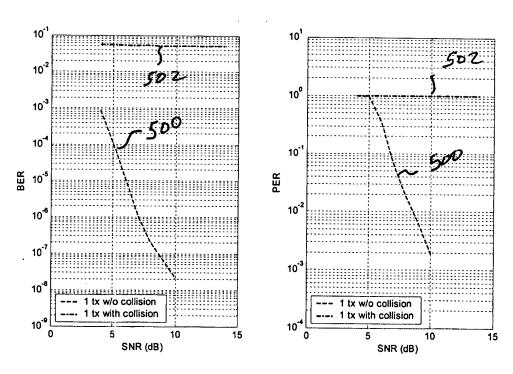
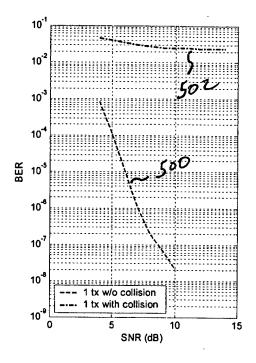


FIG. 6A

FIG. 6B



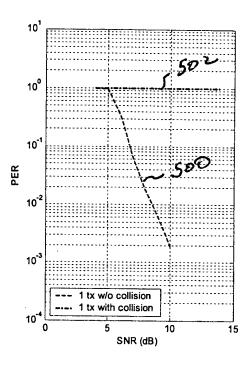
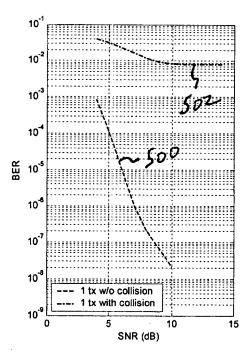


FIG. 7A

FIG. 7B



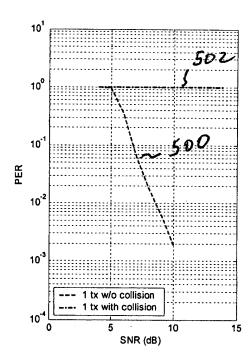
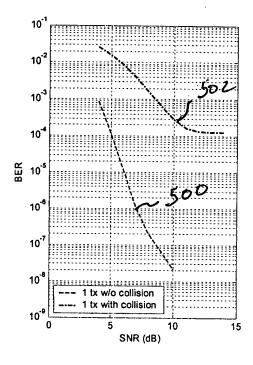


FIG. 8A

FIG. 8B



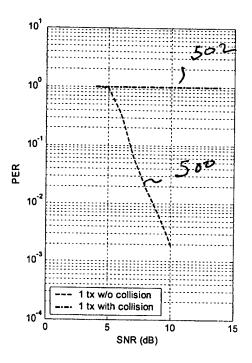
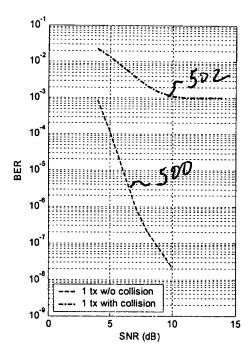


FIG. 9A

FIG. 9B



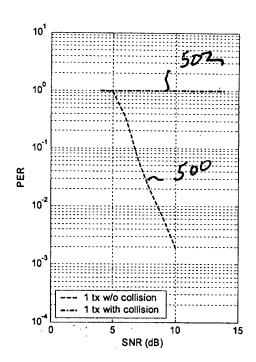
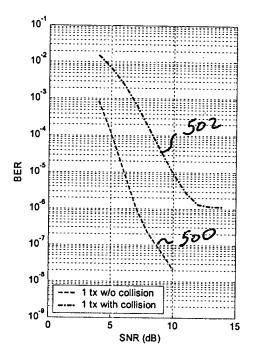


FIG. 10A

FIG. 10B



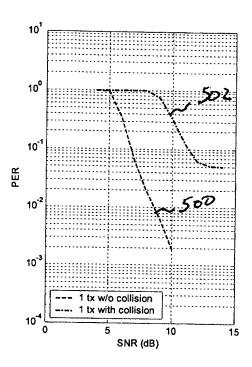
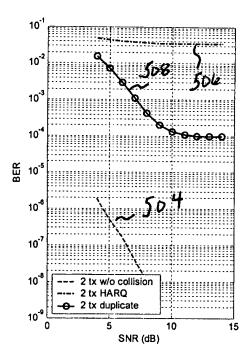


FIG. 11A

FIG. 11B



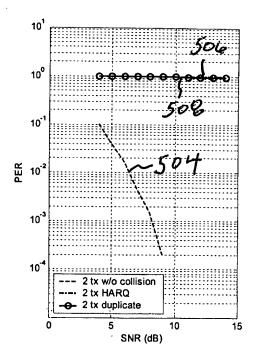
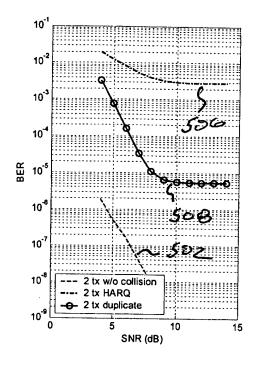


FIG. 12A

FIG. 12B



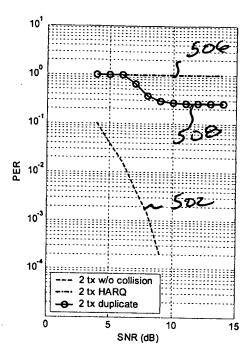
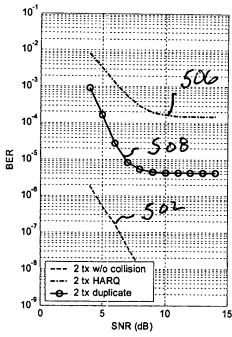


FIG. 13A

FIG. 13B





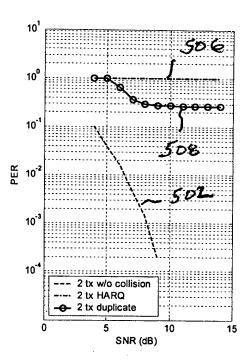
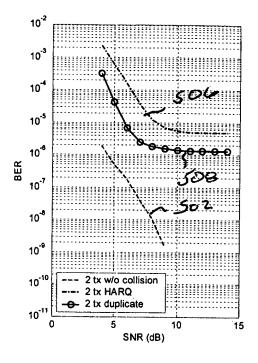


FIG. 14B



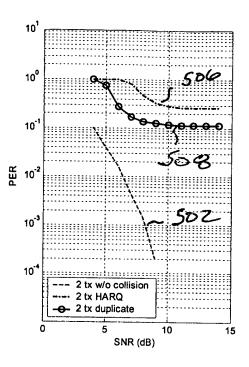
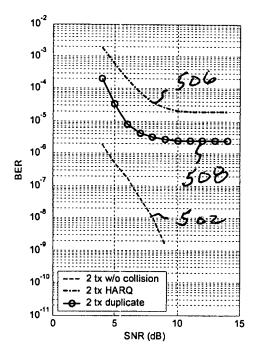


FIG. 15A

FIG. 15B



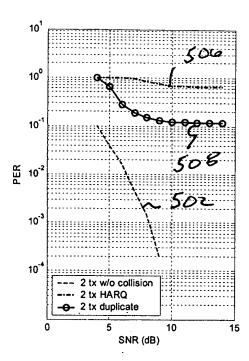
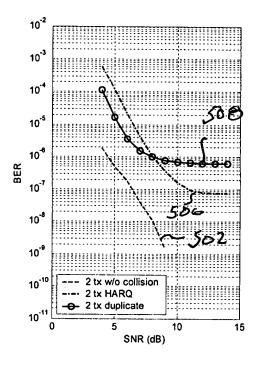


FIG. 16A

FIG. 16B



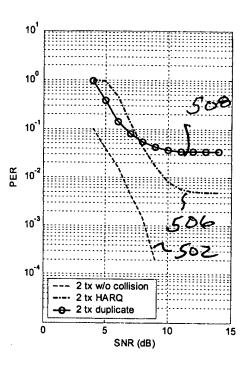


FIG. 17A

FIG. 17B

Recovered

Input Bit ← Stream

FIG. 18

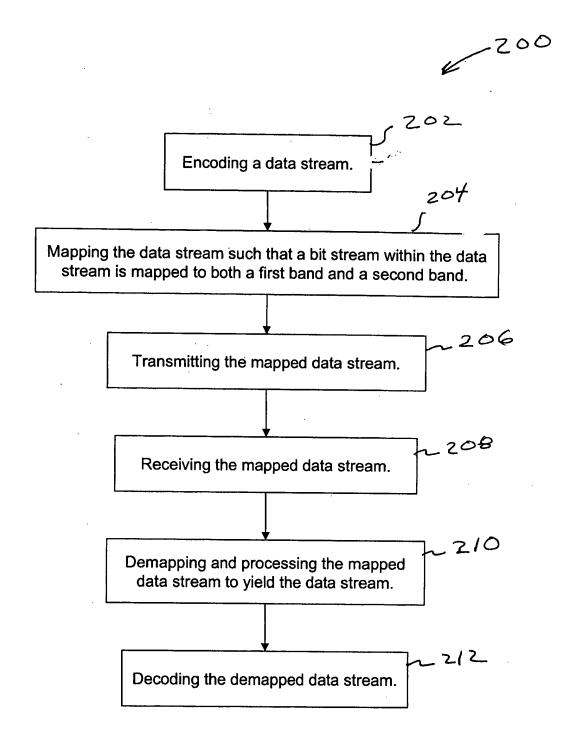


FIG. 19

